DATASHEET 323, REV J

SS-100 SCREENING FLOW

DISCRETE SEMICONDUCTORS

(Not applicable for Axial & MELF Diodes)

All parts procured with JAN-S Screening shall be 100% screened in accordance with the following procedure.

required for stud devices and metallurgically bonded diodesfor power rating > 10W at Tc = 25C - 1- minute hold time does not apply5PIND2052Test Condition A8Serialization9Interim Electrical Parameters-Per device detail specification. 100% Read and Record.10High Temperature Reverse Bias - HTRB1039 Transistors 1042 Power FETS 1038 Diodes and RectifiersCondition A 80% (minimum) of rated VCB (bipolar), VGS (FET) or VDS (FET). Condition B 80% (minimum) of rated VGS. Condition B 80% (minimum) of rated VGS. Condition A 80% (minimum) of rated VGS. Condition a 80% (minimum) of rated VGS. Condition a 80% (minimum) of rated VGS. VRWM; 100% of VRWM if half sine condition is specified. 80%~85% of nominal Vz for Zeners with Vz > 10V; omit for zeners with Vz ≤ 10V11Interim Electrical Parameters 1039 Bipolar Transistors 1042 Power FETs 1038 Diodes, Rectifiers and Zeners 1038 Diodes, Rectifiers and Zeners 1038 Diodes, Rectifiers and Zeners 1038 Diodes, Rectifiers 1038 Diodes, Rectifiers and Zeners 1038 Diodes, Rectifiers 1038 Diodes, Rectifiers 1038 Diodes, Rectifiers 1038 Diodes, Rectifiers 240 hours minimum. Condition B 240 hours mini		TEST / PROCESS	MIL-STD-750 TEST METHOD	TEST CONDITIONS
2069 Power FETS 2072 Transistors, non-glass Diodes 3a Temperature Cycling 1051 3a Temperature Cycling 1051 3b Surge Current 4066 3c Thermal Impedance 3161 Power FETS 3103 IGBT 3131 Bipolar 3101 Diodes 3c Thermal Impedance 3161 Power FETS 3103 IGBT 3131 Bipolar 3101 Diodes 4 Constant Acceleration Not required for stud devices and metallurgically bonded diodes 2006 5 PIND 2052 9 Interim Electrical Parameters - 10 High Temperature Reverse Bias - HTRB 1039 Transistors 1038 Diodes and Rectifiers 10 High Temperature Reverse Bias - HTRB 1039 Transistors 1038 Diodes and Rectifiers 11 Interim Electrical Parameters - 12 Burn-In 1039 Bipolar Transistors 1038 Diodes and Rectifiers 1038 Case nound the Ausay of the Ausay o	1a	Die Visual	2073	Condition B, die form prior to assembly
Image: Surge Current 4066 Condition A or B, as specified. Only applicable if specified. 3b Surge Current 4066 Condition A or B, as specified. 3c Thermal Impedance 3161 Power FETs 3103 IGBT 3131 Bipolar 3101 Diodes Only applicable if specified. 4 Constant Acceleration Not required for stud devices and metallurgically bonded diodes 2006 Y1 direction @ 20,000G, except at 10,000G for power rating > 100W at Tc = 25C - 1- minute hold time does not apply 5 PIND 2052 Test Condition A 8 Serialization - - 9 Interim Electrical Parameters - Per device detail specification. 100% Read and Record. 10 High Temperature Reverse Bias - HTRB 1039 Transistors Condition A 80% (minimum) of rated VCB (bipolar), VGS (FET) or VDS (F	1b	Internal Visual	2069 Power FETs	As specified
3c Thermal Impedance 3161 Power FETs 3103 IGBT 3131 Bipolar 3101 Diodes Only applicable if specified. 4 Constant Acceleration Not required for stud devices and metallurgically bonded diodes 2006 Y1 direction @ 20,000G, except at 10,000G for power rating > 10W at Tc = 25C - 1 - minute hold time does not apply 5 PIND 2052 Test Condition A 8 Serialization - - 9 Interim Electrical Parameters - Per device detail specification. 100% Read and Record. 10 High Temperature Reverse Bias - HTRB 1039 Transistors 1042 Power FETs 1038 Diodes and Rectifiers Condition A 80% (minimum) of rated VCB (bipolation B 80% (minimum) of rated VR or VRWM if half sine condition is specified. 11 Interim Electrical Parameters - As specified, but including all delta parameters, as a minimum. Leakage current shall be measured prior to any other parameter, within 16 hours after removal of applied voltage in HTRB. 12 Burn-In 1039 Bipolar Transistors 1038 Diodes, Rectifiers 1038 Diodes, Rectifiers 1038 Diodes, Rectifiers 1038 Case mount Rectifiers 1038 Case mount Rectifiers 1038 Case mount Rectifiers 1040 Thyristors Condition B 240 hours minimum. Condition A 240 hours minimum. Condition B 240 hours minimum. 13 <td>3a</td> <td>Temperature Cycling</td> <td>1051</td> <td>temperature, whichever is less. 20 cycles, t (extremes) \geq 10 minutes.</td>	3a	Temperature Cycling	1051	temperature, whichever is less. 20 cycles, t (extremes) \geq 10 minutes.
3103 IGBT 3113 IBjolar 3101 Diodes 3103 IGBT 3113 IBjolar 3101 Diodes 4 4 Constant Acceleration Not required for stud devices and metallurgically bonded diodes 2006 Y1 direction @ 20,000G, except at 10,000G for power rating > 10W at Tc = 25C - 1- minute hold time does not apply 5 PIND 2052 Test Condition A 8 Serialization - 9 Interim Electrical Parameters - 10 High Temperature Reverse Bias - HTRB 1039 Transistors 1042 Power FETs 1042 Power FETs 1038 Diodes and Rectifiers Condition A 80% (minimum) of rated VCB (bipolar), VGS (FET) or VDS (FET). Condition B 80% (minimum) of rated VR or VRWM; 100% of VRWM if half sine condition is specified. 11 Interim Electrical Parameters - 12 Burn-In 1039 Bipolar Transistors 1042 Power FETs 1038 Case mount Rectifiers 1038 Case mount Rectifiers 1038 Case mount Rectifiers 1038 Case mount Rectifiers 1040 Thyristors Condition B 240 hours minimum. Condition B 240 hours minimum. Condition B 240 hours minimum. Condition B 240 hours minimum. Condition B 240 hours minimum. 13 Final Electrical - Group A, Subgroup 2, and delta parameters. Glass Rectifiers and Switching Diodes need Scope Display	3b	Surge Current	4066	
required for stud devices and metallurgically bonded diodesfor power rating > 10W at Tc = 25C - 1- minute hold time does not apply5PIND2052Test Condition A8Serialization9Interim Electrical Parameters Bias - HTRB-Per device detail specification. 100% Read and Record.10High Temperature Reverse Bias - HTRB1039 Transistors 1042 Power FETS 1038 Diodes and RectifiersCondition A 80% (minimum) of rated VCB (bipolar), VGS (FET) or VDS (FET). Condition B 80% (minimum) of rated VR or VRWM if half sine condition is specified. 80%~85% of nominal Vz for Zeners with Vz > 10V; omit for zeners with Vz < 10V	3c	Thermal Impedance	3103 IGBT 3131 Bipolar	Only applicable if specified.
8 Serialization - 9 Interim Electrical Parameters - 10 High Temperature Reverse Bias - HTRB 1039 Transistors Condition A 80% (minimum of rated VCB (bipolar), VGS (FET) or VDS (FET), Condition B 80% (minimum) of rated VRS. 10 High Temperature Reverse Bias - HTRB 1042 Power FETs 1042 Power FETs 1038 Diodes and Rectifiers Condition A 80% (minimum) of rated VRS. 11 Interim Electrical Parameters - As specified. 11 Interim Electrical Parameters - 12 Burn-In 1039 Bipolar Transistors 1042 Power FETs 1038 Diodes, Rectifiers and Zeners 1039 Diodes, Rectifiers and Zeners 1038 Case mount Rectifiers Condition B 240 hours minimum. Condition B 240 hours minimum. Condition B 240 hours minimum. 13 Final Electrical - Group A, Subgroup 2, and delta parameters. Glass Rectifiers and Switching Diodes need Scope Display	4	required for stud devices and	2006	
9 Interim Electrical Parameters - Per device detail specification. 100% Read and Record. 10 High Temperature Reverse Bias - HTRB 1039 Transistors Condition A 80% (minimum of rated VCB (bipolar), VGS (FET) or VDS (FET). Condition B 80% (minimum) of rated VRS. Condition A 80% (minimum) of rated VR or VRVMI 1038 Diodes and Rectifiers 11 Interim Electrical Parameters - As specified. 80%~85% of nominal Vz for Zeners with Vz ≤ 10V 11 Interim Electrical Parameters - As specified, but including all delta parameters, as a minimum. Leakage current shall be measured prior to any other parameter, within 16 hours after removal of applied voltage in HTRB. 12 Burn-In 1039 Bipolar Transistors 1042 Power FETs 1038 Diodes, Rectifiers and Zeners 1038 Diodes, Rectifiers and Zeners 2040 hours minimum. Condition B 240 hours minimum. Condition A 240 hours minimum. 1040 Thyristors 13 Final Electrical - Group A, Subgroup 2, and delta parameters. Glass Rectifiers and Switching Diodes need Scope Display	5	PIND	2052	Test Condition A
Image: 100 minimumInterim Electrical Parameters 10381039 Transistors 1042 Power FETs 1038 Diodes and RectifiersCondition A 80% (minimum of rated VCB (bipolar), VGS (FET) or VDS (FET). Condition A 80% (minimum) of rated VGS. Condition A 80% (minimum). Leakage current shall be measured prior to any other parameter, within 16 hours after removal of applied voltage in HTRB.12Burn-In1039 Bipolar Transistors 1042 Power FETs 1038 Diodes, Rectifiers and Zeners 1038 Diodes emount Rectifiers 1040 ThyristorsCondition B 240 hours minimum. Condition B 240 hours minimum. 240 hours minimum. Condition B 240 hours minimum. Condition B 240 hours minimum. Condition B 240 hours minimum. 240 hours m	8	Serialization	-	-
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Image: space	10		1042 Power FETs	(bipolar), VGS (FET) or VDS (FET). Condition B 80% (minimum) of rated VGS. Condition A 80% (minimum) of rated VR or VRWM; 100% of VRWM if half sine
12Burn-In1039 Bipolar Transistors 1042 Power FETs 1038 Diodes, Rectifiers and Zeners 1038 Case mount Rectifiers 1040 ThyristorsCondition B 240 hours minimum. Condition B 240 hours minimum. 				
1042 Power FETs 1042 Power FETs Condition A 240 hours minimum. 1038 Diodes, Rectifiers and Zeners 1038 Case mount Rectifiers Condition B 240 hours minimum. 1039 Total Case 1040 Thyristors Condition B 240 hours minimum. 13 Final Electrical - Group A, Subgroup 2, and delta parameters. Glass Rectifiers and Switching Diodes need Scope Display	11	Interim Electrical Parameters	-	parameters, as a minimum. Leakage current shall be measured prior to any other parameter, within 16 hours after removal of
Glass Rectifiers and Switching Diodes need Scope Display	12	Burn-In	1042 Power FETs 1038 Diodes, Rectifiers and Zeners 1038 Case mount Rectifiers	Condition A 240 hours minimum. Condition B 240 hours minimum.
13a PDA 5% Max	13	Final Electrical	-	
	13a	PDA		5% Max

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13b	Other Electrical	-	As specified; Group A, subgroup 3
14	Hermetic Seal a. Fine b. Gross	1071	Fine leak – not required for Double Plug Diodes
15	Radiography	2076	-
16	External Visual	2071	After complete marking, prior to lot acceptance.
17	Case Isolation	To be performed on case isolated packages.	As specified.

Notes: 1) Sequence and testing varies per device.

2) For diode bridges pre-cap visual is performed at the bridge assembly level prior to potting.

3) Flow in accordance with slash sheet may be used if applicable.

SS-100 SCREENING FLOW AXIAL AND MELF ZENER DIODES

All parts procured with JAN-S Screening shall be 100% screened in accordance with the following procedure.

SCREEN NUMBER	TEST/PROCESS	MIL-STD-750 TEST METHOD	TEST CONDITION
1a	Die Visual	2073	-
1b	Internal Visual	2074	-
3a	Temperature Cycling (Thermal Shock)	1051	No dwell time is required at +25°C. Test condition C, or maximum storage temperature, whichever is less, 20 cycles.
3b	Surge Current	4066	Condition B as specified
	If specified on Slash Sheet		
3c	Thermal Impedance	3101	As specified on Slash Sheet
7	Hermetic Seal	1071	-
8	Serialization	-	-
9	Electrical Tests	-	As specified; Read and Record.
10	High Temperature Reverse	1038A	Test Condition A
	Bias (HTRB)		T_{A} = 150°C ; t = 48 hrs; V = 80% of V_{Z\text{NOM}}
11	Electrical Tests and delta parameters for PDA	-	As specified; Read and Record.
12	Burn-In	1038B	Test Condition B
			T_{A} = +75°C $_{MAX.}$; t = 240 hrs. ; I _{Z (MIN)} \geq as specified ; T _{J MIN} = as specified; adjust I _Z and/or T _A to achieve the required T _{J MIN}
13a	Electrical Tests and delta parameters for PDA	-	As specified; Read and Record ; Group A, subgroup 2
13b	Other Electrical	-	As specified; Group A, subgroup 3
15	Radiography	2076	-
16	External Visual	2071	After complete marking, prior to lot acceptance.

SS-100 SCREENING FLOW

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SS-100 SCREENING FLOW HYBRIDS

Reference: MIL-PRF-38534, Class K

	SCREEN	MIL-STD-883 METHOD	CONDITIONS
1	Non-destructive Pull Test	2023	100%
2	Internal Visual	2017	Condition A
3	Temperature Cycling	1010	Condition C
4	Constant Acceleration	2001	Condition A (min) Y1 orientation only.
5	PIND	2020	Condition A
6	Serialization	-	-
7	Pre burn in Electrical	-	Per device detailed specification.
	Parameters		Read and record.
8	Burn-in	1015	320 hours at 125□ C minimum, divided
			equally into 2 successive burn-ins.
9	Final Electrical Parameters	-	Per device detailed specification.
			Subgroups 1, 2, 3 minimum
			Read and record
10	PDA Calculation	-	2% or 1 device, calculated on failures from
			second burn-in only.
11	Seal:	1014	Fine Leak
			Gross leak
12	Radiography	2012	As specified
13	External Visual, Mechanical	2009	As specified

SS-100 SCREENING FLOW

MICROCIRCUITS

Reference: MIL-PRF-38535, Class S and MIL-STD-883, Test Method 5004 Class S

	SCREEN	MIL-STD-883 METHOD	CONDITIONS
1	Non-destructive bond pull	2023	100%
2	Internal Visual	2010	Condition A
3	Temperature Cycling	1010	Condition C
4	Constant Acceleration	2001	Condition E (min) Y1 orientation only.
4.1	Visual Inspection	2009	-
5	PIND	2020	Condition A
6	Serialization	-	-
7	Pre burn in Electrical	-	Per device detailed specification.
	Parameters		Read and record only when delta required.
8	Burn-in	1015	240 hours at 125 C minimum.
9	Interim (post burn in) Electrical	-	Per device detailed specification.
	Parameters		Read and record.
10	PDA Calculation	-	5% or 1 device whichever is greater
			(Group A subgroup 1 + deltas) plus
			3% or 1 device whichever is greater.
			(Functional parameters at 25°C)
11	Final Electrical Parameters	-	Per device detailed specification
			Read and record.
12	Seal:	1014	-
	a. Fine		
	b. Gross		
13	Radiographic	2012	Two views
14	External Visual, Mechanical	2009	-

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