**DATASHEET 5487, REV F** 

# 1200 VOLT, 40 AMP MOSFET FULL-BRIDGE MODULE

### **Features**

- Isolated base plate
- Light weight low profile standard package
- Aluminum Nitride substrate
- High temperature engineering plastic shell construction
- Enhanced die coating
- Die back metal change from silver to gold



### **ELECTRICAL CHARACTERISTICS PER MOSFET LEG**

SYMBOL	PARAMETER	MIN	TYP	MAX	UNIT	
MOSFET SPECIFICATIONS						
BV <sub>DSS</sub>	Drain to Source Breakdown Voltage $I_D = 100 \mu A, V_{GS} = 0 V$	1200	-	-	V	
ID	Continuous Drain Current $T_C = 25^{\circ}C$ $T_C = 100^{\circ}C$	-	-	60 40	Α	
I <sub>D(pulse)</sub>	Pulsed Drain Current, 1ms	-	-	160	Α	
V <sub>GS</sub>	Gate to Source Voltage	-	-	-10/+25	V	
Igss	Gate-Source Leakage Current , V <sub>GS</sub> = +20V / -5V	-	-	250	nA	
V <sub>GS(th)</sub>		2.0 1.4	3.2 2.3	4.0 3.2	V	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current VDS = 1200 V, VGS=0V	-	1	100	μΑ	
R <sub>DS(on)</sub>	Drain-Source On-State Resistance $T_J = 25^{\circ}\text{C}$ $I_D = 40\text{A}, V_{GS} = 20\text{V}$ $T_J = 150^{\circ}\text{C}$	-	47 98	56 118	mΩ	
Ciss Coss Crss	Input Capacitance Output Capacitance Reverse Transfer Cap. VDS = 1000 V, VGS = 0 V, f = 1 MHz, VAC = 25 mV	- - -	2287 157 8	- - -	pF	
$\begin{array}{c} t_{D(\text{on})} \\ t_{R} \\ t_{D(\text{off})} \\ t_{F} \end{array}$	Turn On Delay Time Rise Time Turn Off Delay Time Fall Time $V_{DS} = 800 \text{ V}, V_{GS} = 0 \text{ V}, V_{T} = 1 \text{ Win 12}, V_{AC} = 25 \text{ HiV}$	- - - -	15 52 26 34	- - - -	ns	
Eas	Avalanche Energy, Single Pulse I <sub>D</sub> = 40A, V <sub>DS</sub> = 50V	-	2	-	J	
Eon Eoff	Turn on Energy Loss Turn off Energy Loss $V_{DS}$ = 800 V, $I_{D}$ = 40A, $V_{GS}$ = -5/+20V, $R_{G}$ = 2.5 $\Omega$ , L = 80 $\mu$ H	-	1000 400	-	μJ	
R <sub>G(int)</sub>	Internal Gate Resistance f = 1MHz, V <sub>AC</sub> = 25mV	-	1.8	-	Ω	
Qgs Qgd Qg	Gate to Source Charge Gate to Drain Charge Total Gate Charge V <sub>DS</sub> = 800 V, I <sub>D</sub> = 40A, V <sub>GS</sub> = -5/+20V	-	29 45 131	-	nC	

## SENSITRON SEMICONDUCTOR

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### **REVERSE DIODE CHARACTERISTICS**

(T<sub>J</sub>=25°C UNLESS OTHERWISE SPECIFIED)

SYMBOL	PARAMETER		TYP	MAX	UNIT		
DIODE SPECIFICATIONS							
V <sub>SD</sub>	$\begin{array}{ccc} \mbox{Diode Forward Voltage} & \mbox{T}_{\mbox{\scriptsize J}} = 25^{\circ} \\ \mbox{V}_{\mbox{\scriptsize GS}} = -5\mbox{V}, \mbox{I}_{\mbox{\scriptsize J}} = 20\mbox{A} & \mbox{T}_{\mbox{\scriptsize J}} = 150 \end{array}$	_	4.2 3.7	4.5 4.0	V		
Is	Continuous Forward Current, T <sub>J</sub> = 25°	C -	-	60	Α		
t <sub>rr</sub>	Reverse Recovery Time $V_{GS} = -5V$ , $I_{SD} = 40A$ , $V_{R} = 800V$ , $di/dt = 1406A/ \mu s$	-	63	-	ns		
Qrr	Reverse Recovery Charge V <sub>GS</sub> = -5V, I <sub>SD</sub> = 40A, V <sub>R</sub> =800V, di/dt = 1406A/ µs	-	964	-	nC		
Irrm	Peak Reverse Recovery Current $V_{GS} = -5V$ , $I_{SD} = 40A$ , $V_{R} = 800V$ , $di/dt = 1406A/ \mu s$	-	18	-	А		

### **ZVS SIC DIODE CHARACTERISTICS**

(T<sub>J</sub>=25°C UNLESS OTHERWISE SPECIFIED)

240 010 DIODE CHARACTERIOTICO			(TJ=25 C ONLESS OTTLEN VISE STECHTED)					
SYMBOL	PARAMETER		MIN	TYP	MAX	UNIT		
DIODE SPECIFICATIONS								
V <sub>RRM</sub>	Repetitive Peak Reverse Voltage		1200	-	-	V		
V <sub>RSM</sub>	Surge Peak Reverse Voltage		1300	-	-	V		
V <sub>R</sub>	DC Peak Blocking Voltage		1200	-	-	V		
I <sub>F</sub>	Continuous Forward Current,	T <sub>J</sub> = 150°C	-	-	5	Α		
I <sub>FRM</sub>	Repetitive Peak Forward Surge Current tp= 10ms, Half Sine Pulse	T <sub>C</sub> = 25°C T <sub>C</sub> = 110°C	-	-	26 18	А		
I <sub>FSM</sub>	Non-Repetitive Forward Surge Current tp= 10ms, Half Sine Pulse	$T_C = 25$ °C $T_C = 110$ °C	-	-	46 36	Α		
VF	Forward Voltage I <sub>F</sub> = 5A	T <sub>J</sub> = 25°C T <sub>J</sub> = 150°C	-	1.4 1.9	1.8 3.0	V		
I <sub>R</sub>	Reverse Current V <sub>R</sub> = 1200V	T <sub>J</sub> = 25°C T <sub>J</sub> = 150°C	-	20 40	150 300	μΑ		
Qc	Total Capacitive Charge $V_{R=}$ 800V, $I_F$ = 5A, di/dt = 200A/ $\mu$ s, $T_J$ = 25 $^{\circ}$ C		-	27	-	nC		
С	Total Capacitance $V_R = 0V$ , $T_J = 25$ °C, $f = 1MHz$ $V_R = 400V$ , $T_J = 25$ °C, $f = 1MHz$ $V_R = 800V$ , $T_J = 25$ °C, $f = 1MHz$		-	390 27 20	-	pF		

Note: Production units are only tested at room temperature. Low/High temperature operation is guaranteed by design.

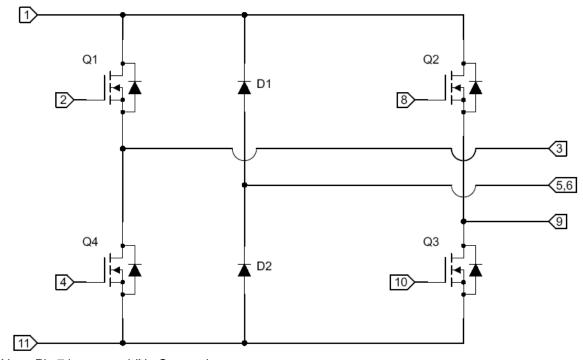
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### THERMAL AND MECHANICAL CHARACTERISTICS

SYMBOL	PARAMETER	MIN	TYP	MAX	UNIT
R <sub>0</sub> JB_M	MOSFET Junction-to-Base Plate Thermal Resistance Per Leg	-	0.30	0.36	°C/W
$R_{\theta JB\_D}$	Diode Junction-to-Base Plate Thermal Resistance Per Leg	-	1.60	1.76	°C/W
V <sub>iso</sub>	Isolation to Base Plate	-	-	2500	VDC
TJ	Operating Junction Temperature	-55	-	150	°C
T <sub>STG</sub>	Storage Temperature	-55	-	150	°C
	Mounting Torque for Module Mounting	3	-	4	in-lbs.
	Weight	-	10	-	g

Recommended TIM = Laird Tgon 805

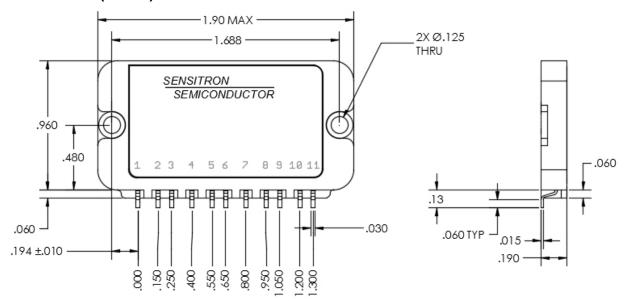
### **Schematic Diagram:**



Note: Pin 7 is not used (No Connect)

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#### **Mechanical Outline (inches):**



TOLERANCE UNLESS OTHERWISE NOTED:

 $.XX = \pm .010$ 

 $.XXX = \pm .005$ 

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